Test Bench Explanations:

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| 00000000 | Print contents of R0; **Printout is :“ 0”**; R0 is initialized to ‘0’. |
| 00001000 | Print contents of R1; **Printout is: “ 0”**; R1 is initialized to ‘0’. |
| 00010000 | Print contents of R2; **Printout is: “ 0”**; R2 is initialized to ‘0’. |
| 00011000 | Print contents of R3; **Printout is: “ 0”**; R3 is initialized to ‘0’. |
| 11000001 | Load immediate value of ‘1’ into R0. |
| 00000000 | Print contents of R0; **Printout is: “ 1”**; |
| 11010111 | Load immediate value of ‘7’ into R1. |
| 00001000 | Print contents of R1; **Printout is: “ 7”**; |
| 11101000 | Load immediate value of ‘-8’ into R2. |
| 00010000 | Print contents of R2; **Printout is: “ -8”**; |
| 01110001 | R3 = R0 + R1; |
| 00011000 | Print contents of R3; **Printout is: “ 8”**; |
| 01111111 | R3 = R3 + R3; |
| 00011000 | Print contents of R3; **Printout is: “ 16”**; |
| 01111110 | R3 = R3 + R2; |
| 00011000 | Print contents of R3; **Printout is: “ 8”**; |
| 01111110 | R3 = R3 + R2; |
| 01111110 | R3 = R3 + R2; |
| 00011000 | Print contents of R3; **Printout is: “ -8”**; |
| 10110100 | R3 = R1 – R0; |
| 00011000 | Print contents of R3; **Printout is: “ 6”**; |
| 11000111 | Load immediate value of ‘7’ into R0. |
| 10111100 | R3 = R3 – R0; |
| 00011000 | Print contents of R3; **Printout is: “ -1”**; |
| 00011001 | Print contents of R3; **Printout is: “ -1”**; Bottom three bits are “don’t care.” |
| 00011010 | Print contents of R3; **Printout is: “ -1”**; Bottom three bits are “don’t care.” |
| 00011100 | Print contents of R3; **Printout is: “ -1”**; Bottom three bits are “don’t care.” |
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